


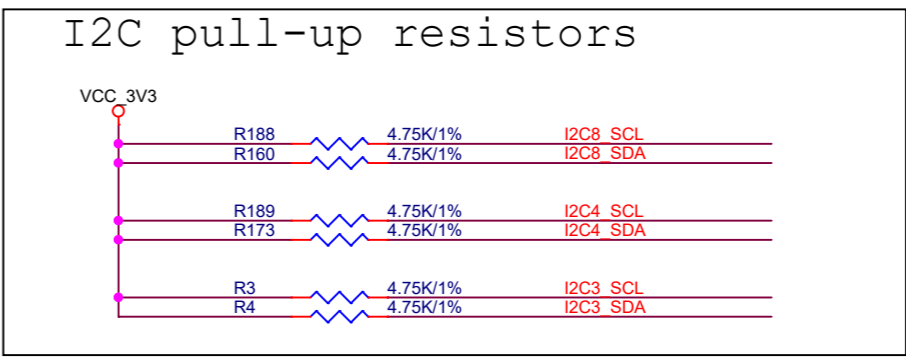
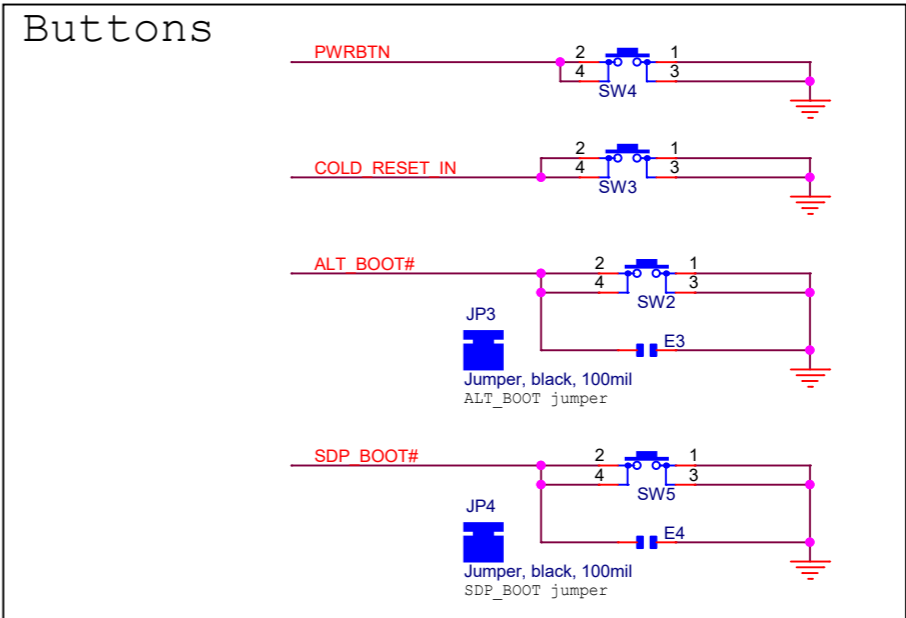
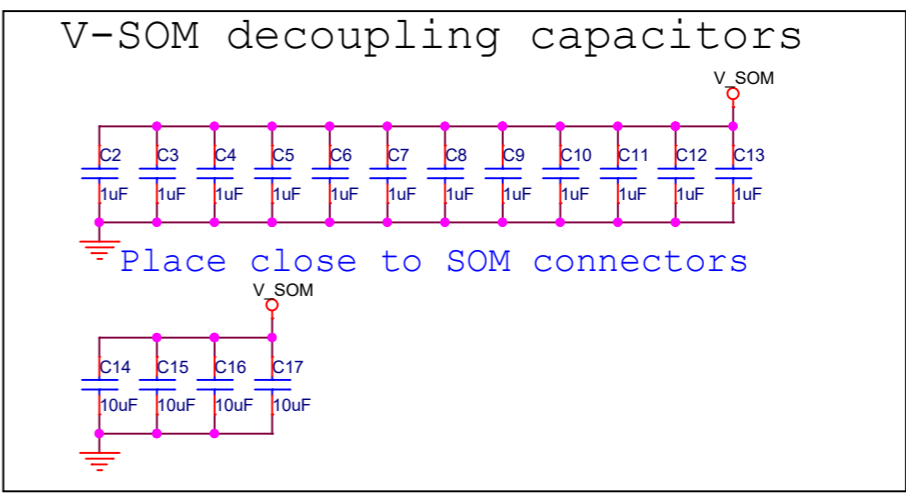
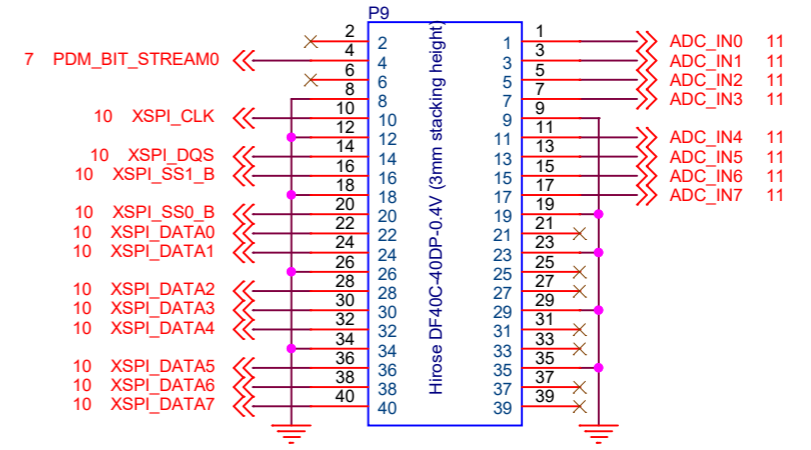
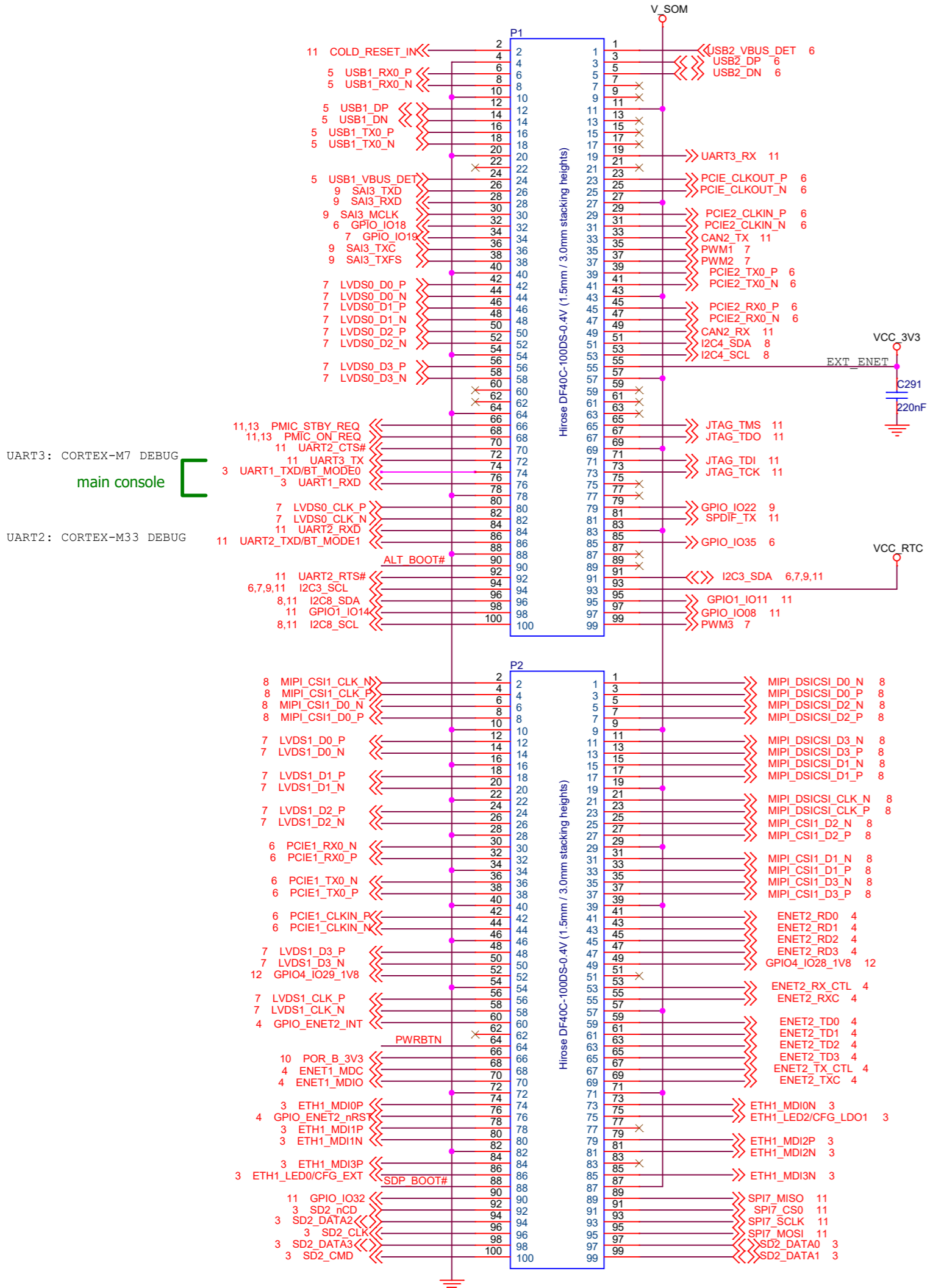
# SB-UCMIMX95

Page	Description
01	Index and Block Diagram
02	SOM connectors
03	Console, GbE, micro-SD
04	Carrier-board Ethernet
05	USB #1 and SDP
06	USB #2 and PCIe
07	LVDS and MPI-DSI
08	MIPI-CSI
09	Audio codec
10	Octal SPI flash
11	I/O headers, JTAG, GPIO exp.
12	LEDs
13	Power
14	DC input and spacers

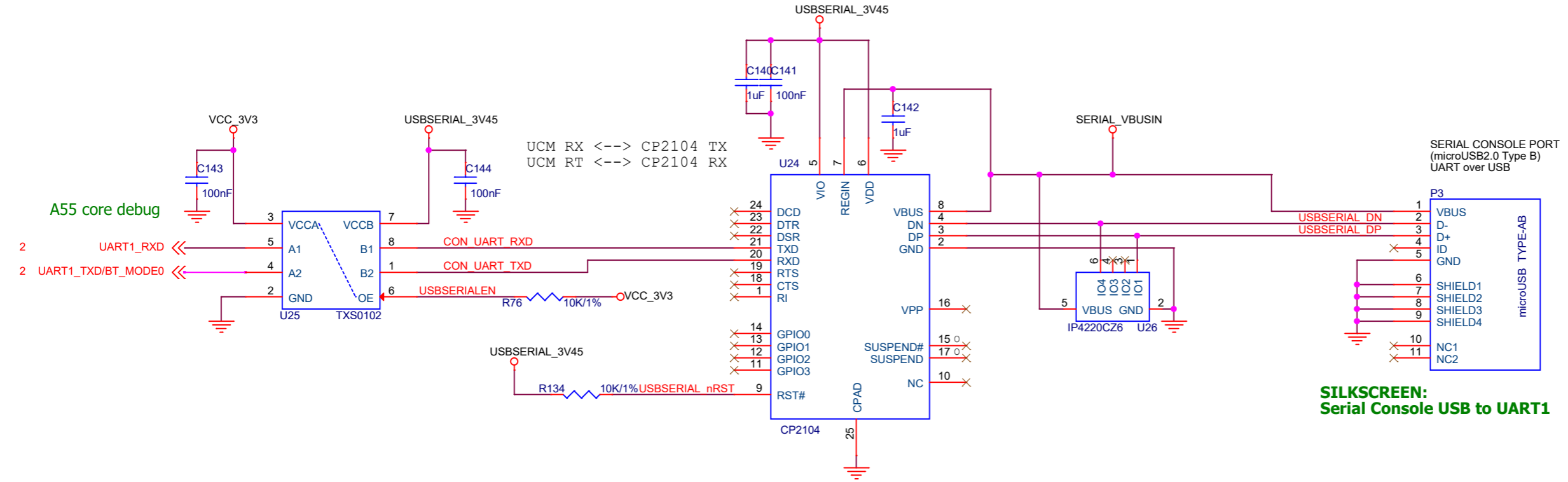
CH #	I2C devices
I2C3	EEPROM, PCA9500, codec, touch,
I2C4	Camera
I2C8	Camera, header

PCB1  
  
PCB, SB-UCMIMX95, Rev 1.0  
P/N = 188C04550

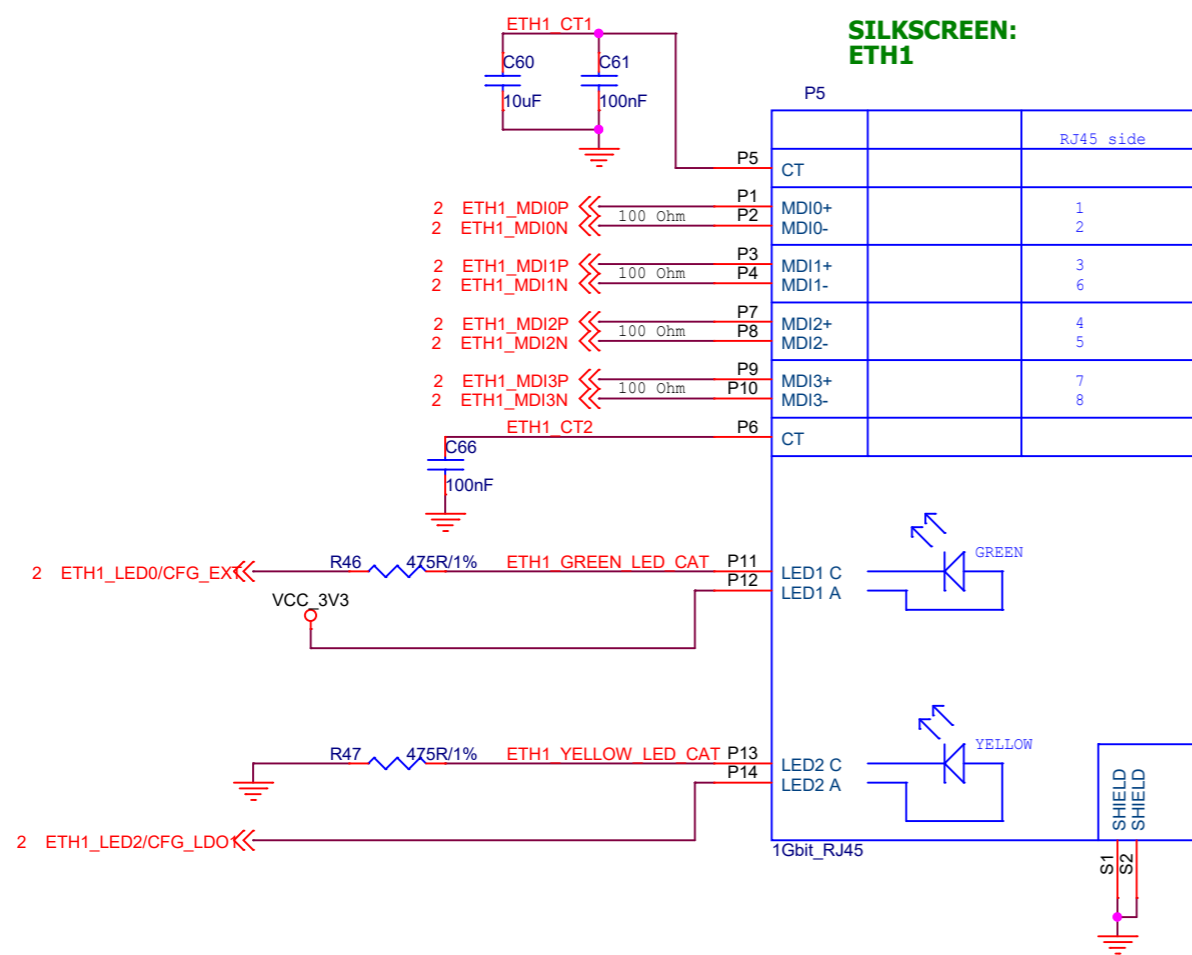
ZZ1  
  
PARSER\_VERSION\_1.0



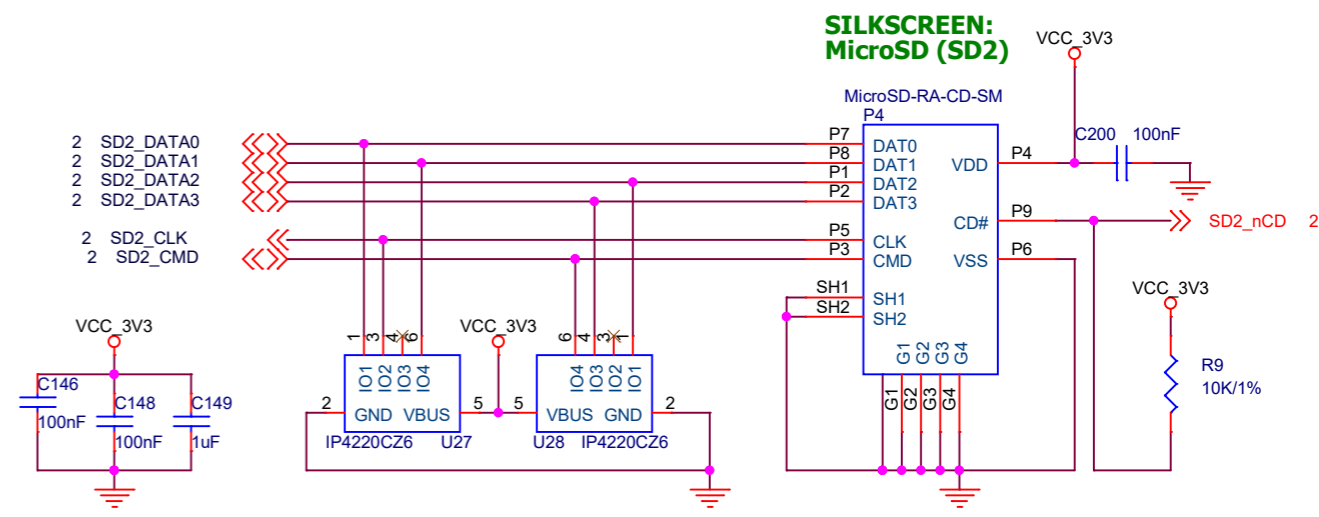
Serial debug console



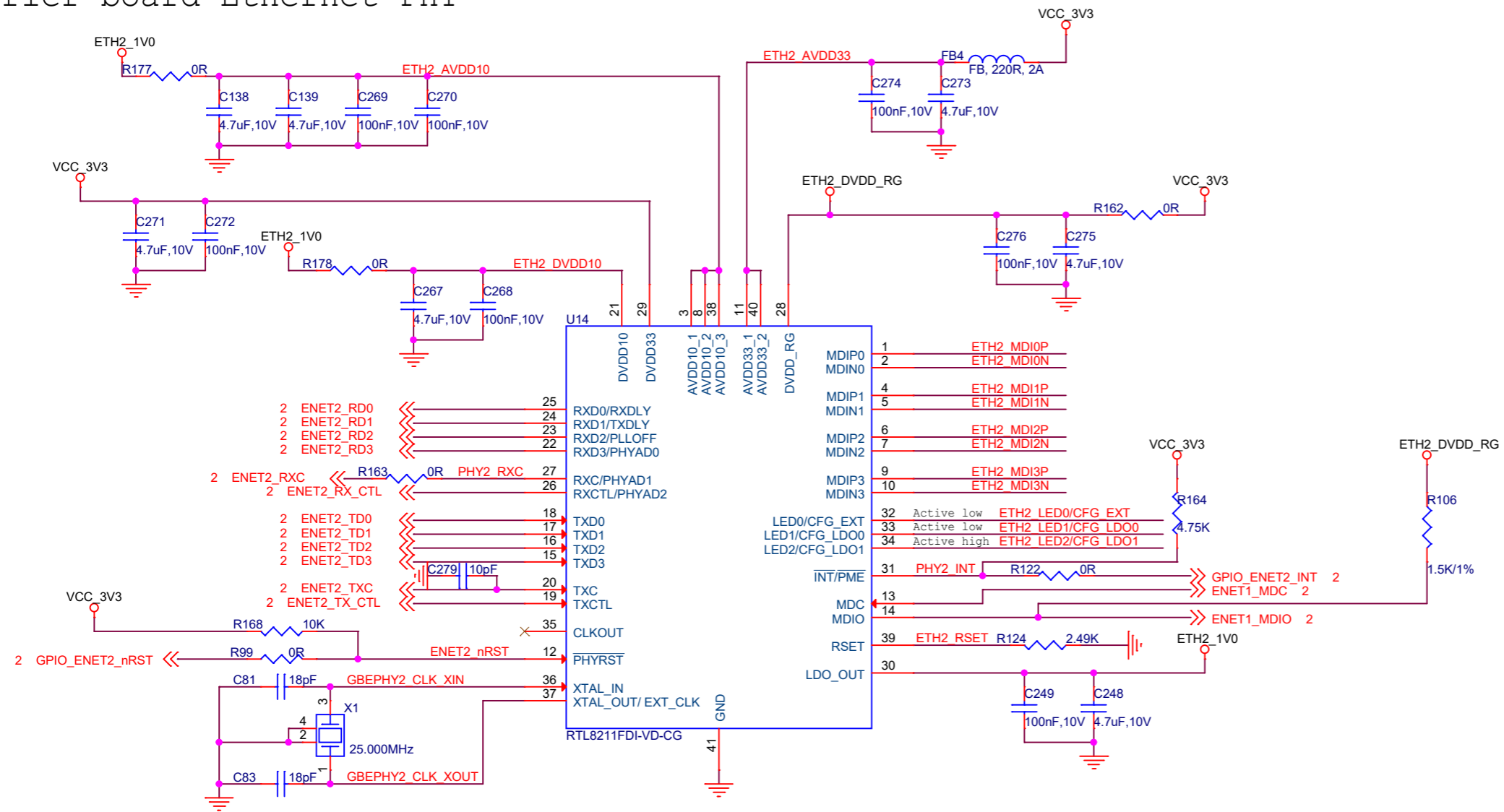
UCM Ethernet connector



Micro-SD socket (bootable)

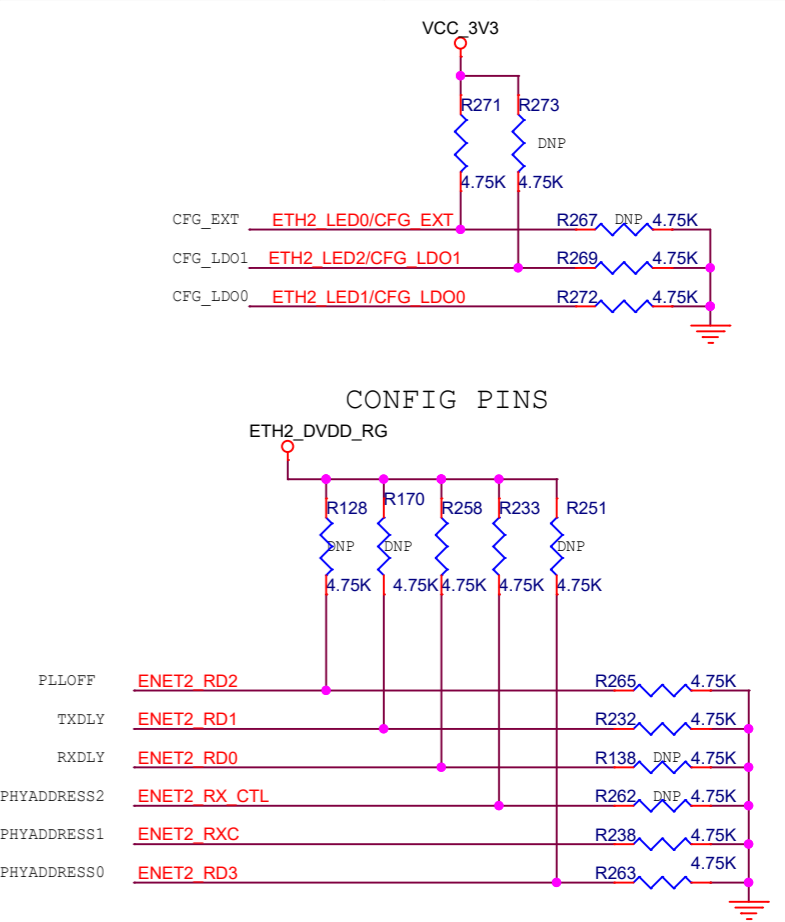


Carrier-board Ethernet PHY

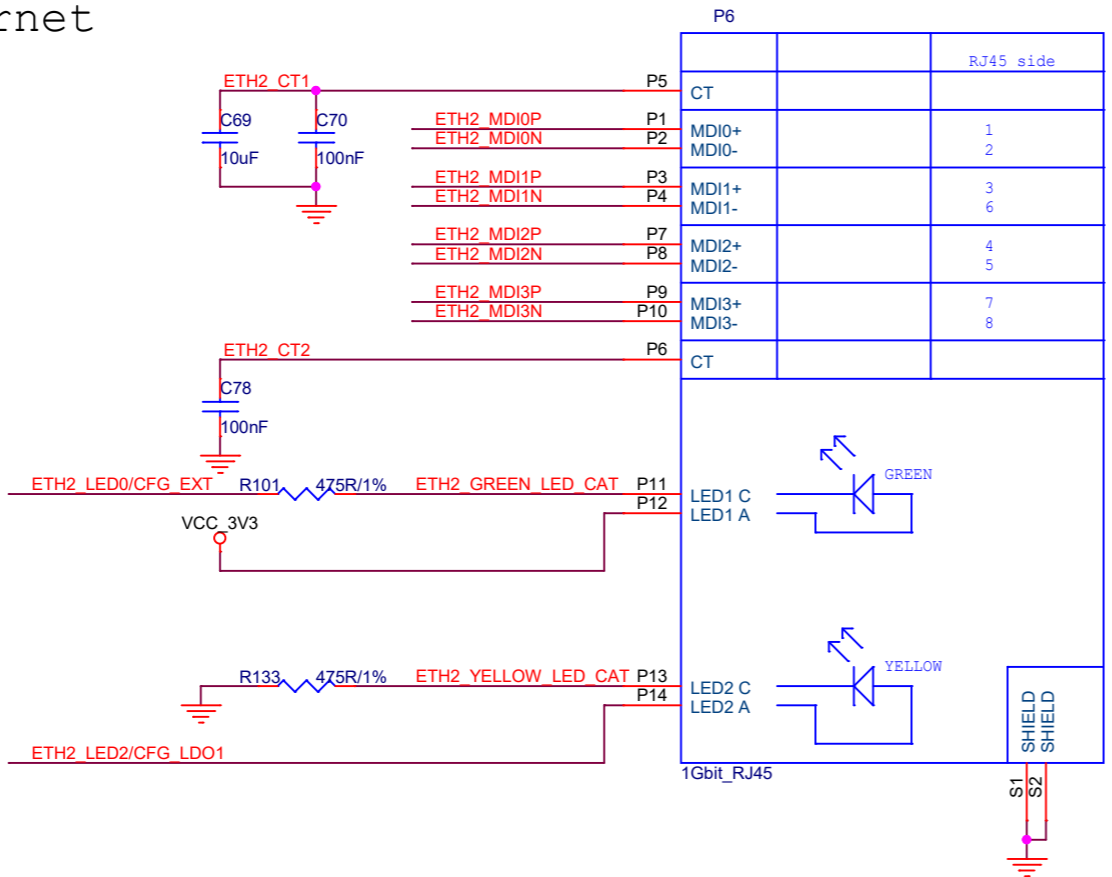


Power-on strap pins CFG

RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V	1	00
External 1.8V	1	10
Internal 1.8V	0	10



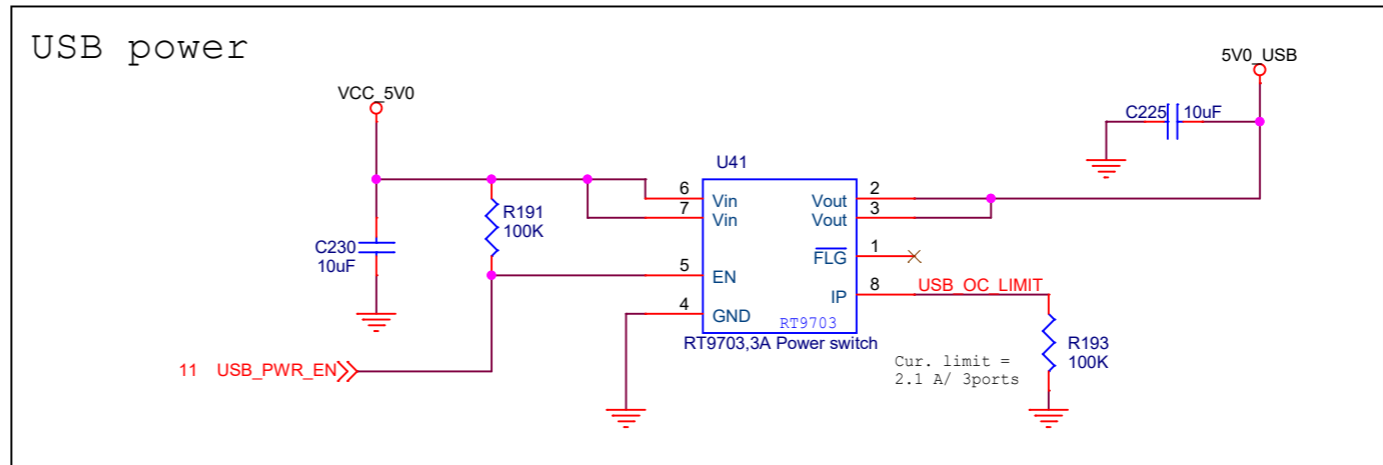
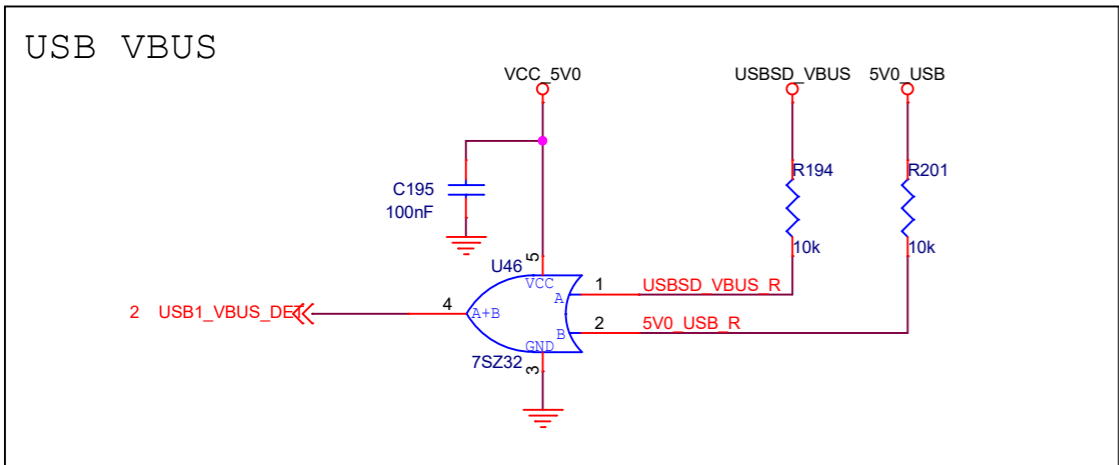
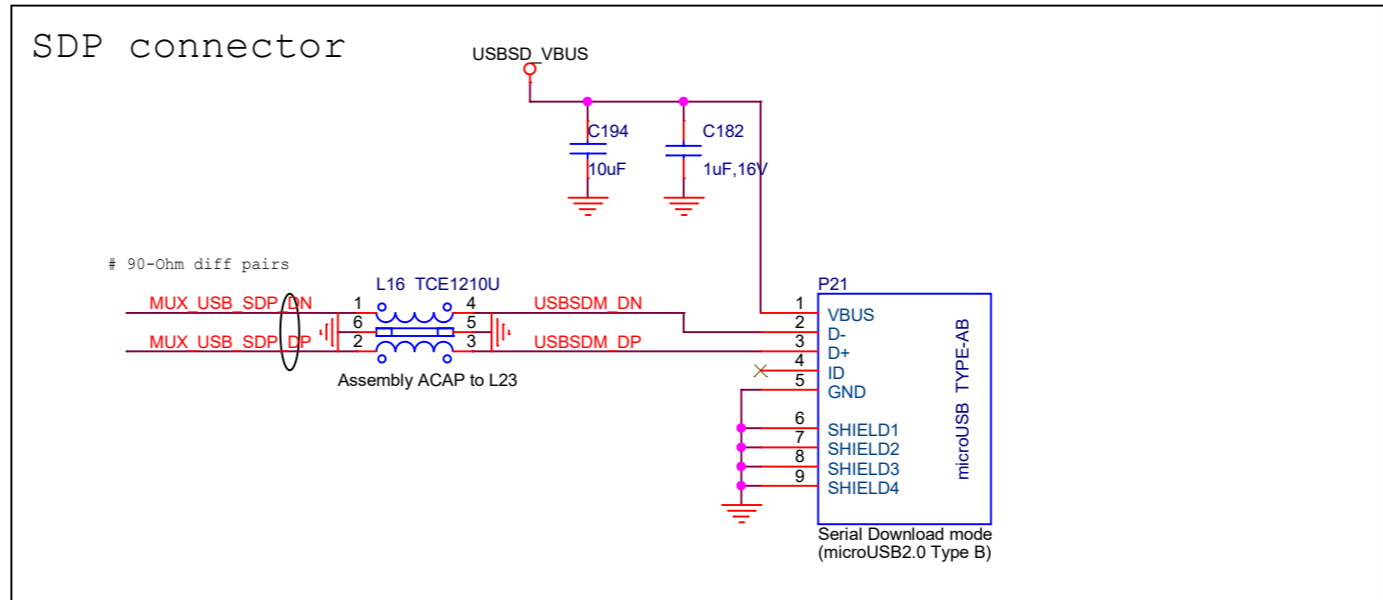
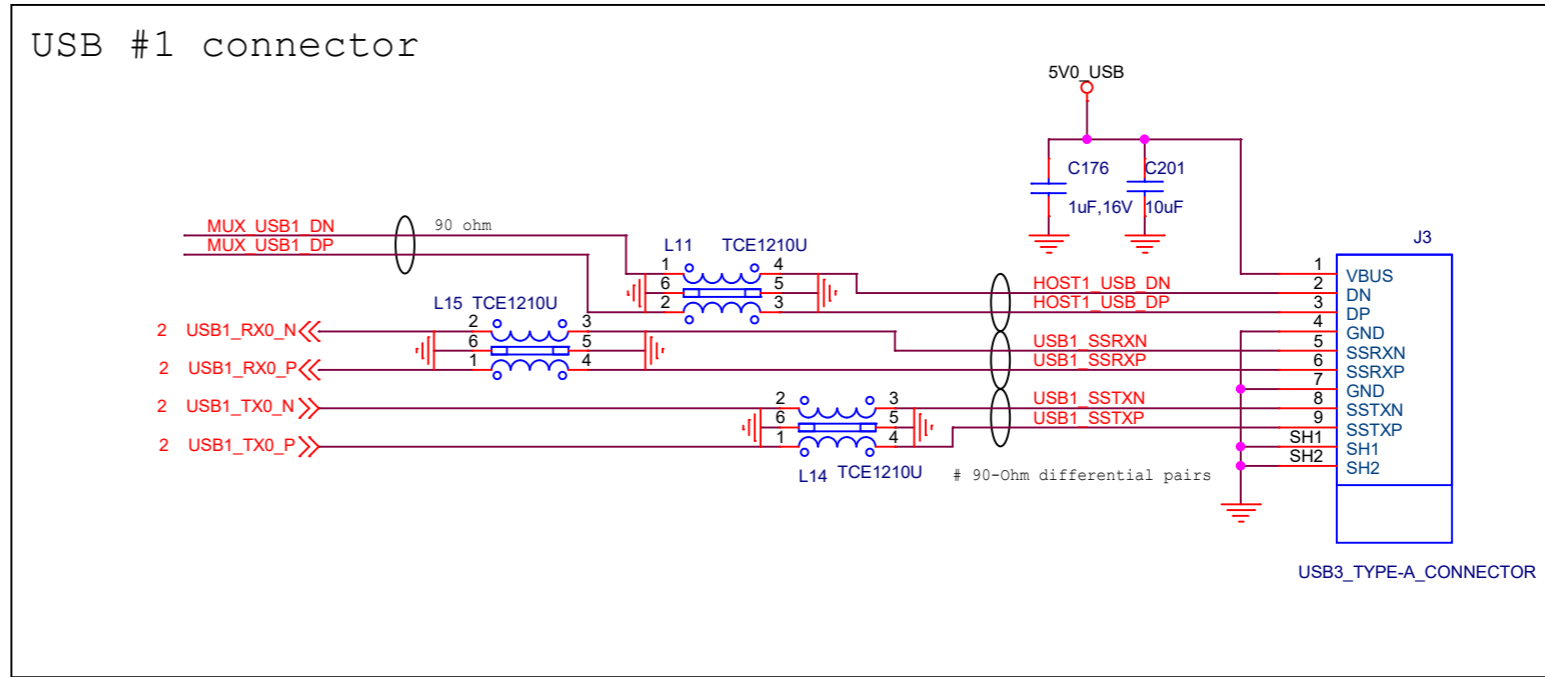
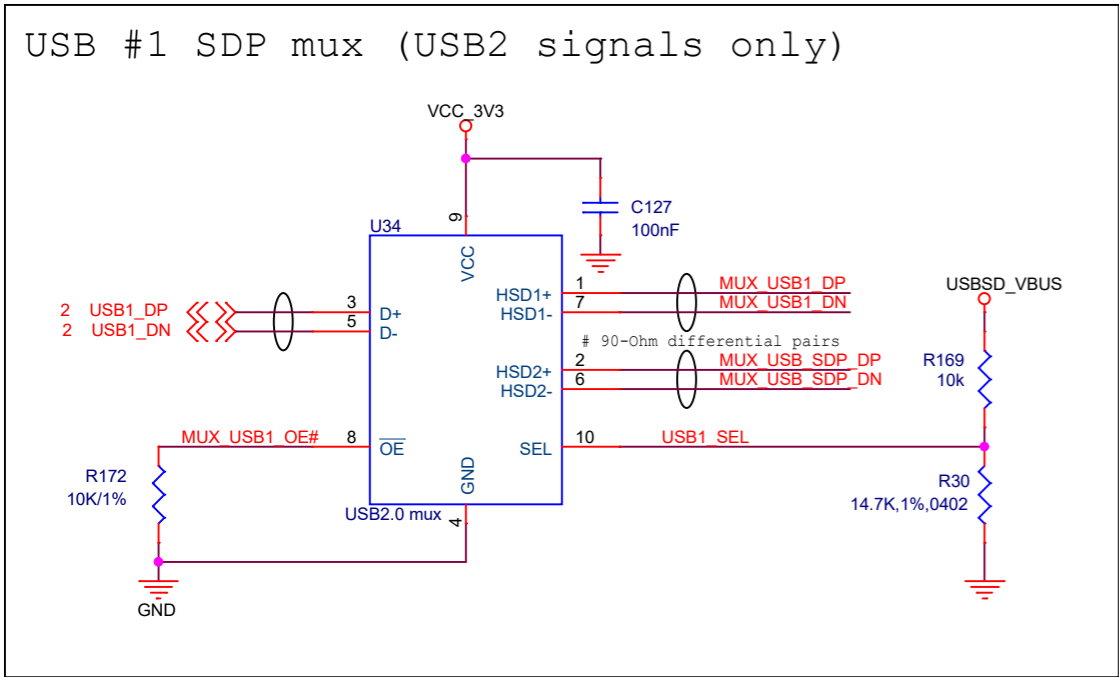
Carrier-board Ethernet connector



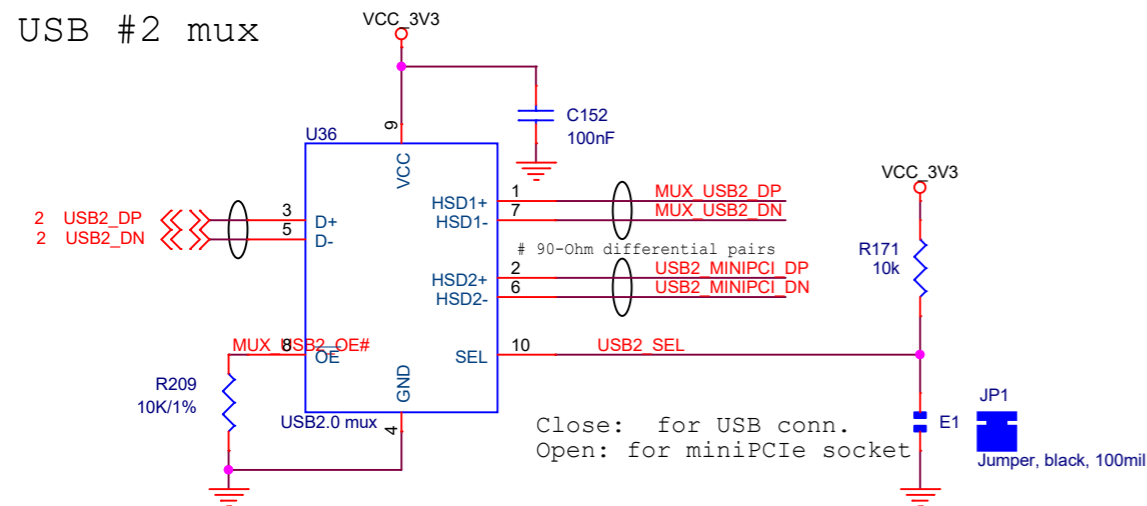
PHY ADDRESS = [1,0,0]  
PHY support addresses from 00001 to 00111.  
PHY address 0 is a broadcast from the MAC

PLLOFF: Pull-up to disable PLL @ ALDPS mode.

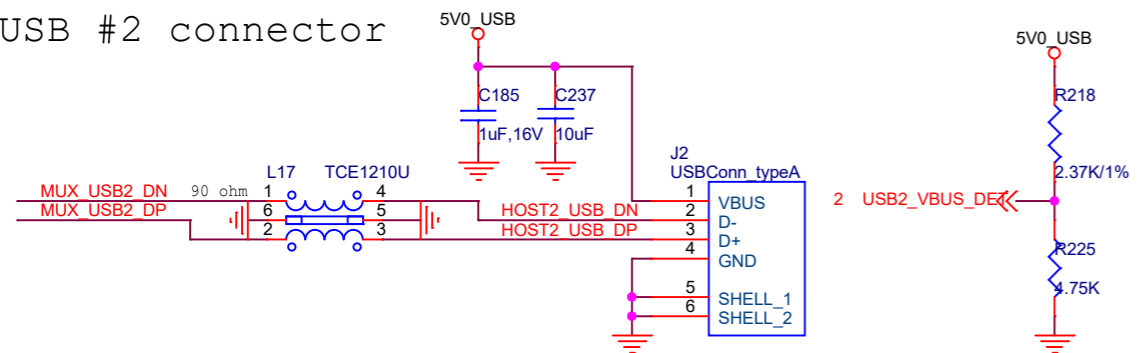
Enable internal delays  
TX delay is disabled  
RX delay is enabled



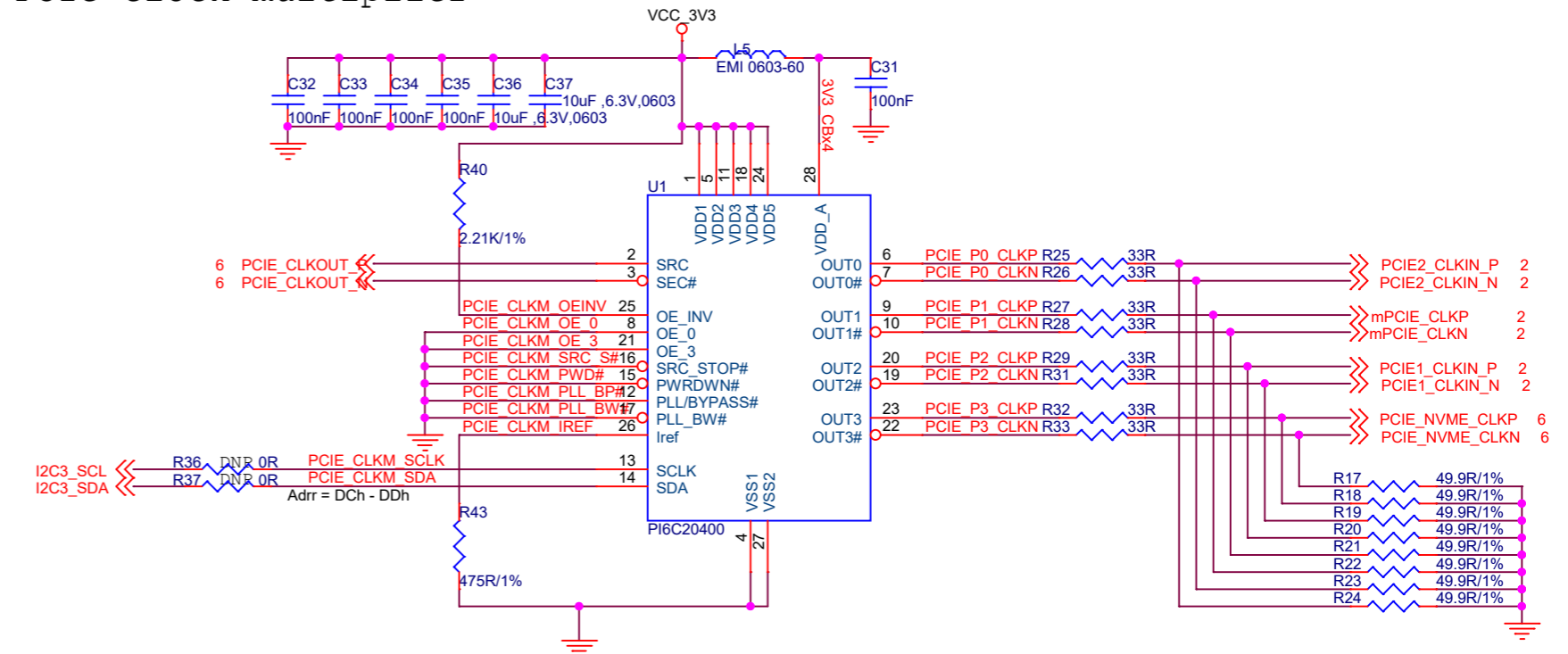
## USB #2 mux



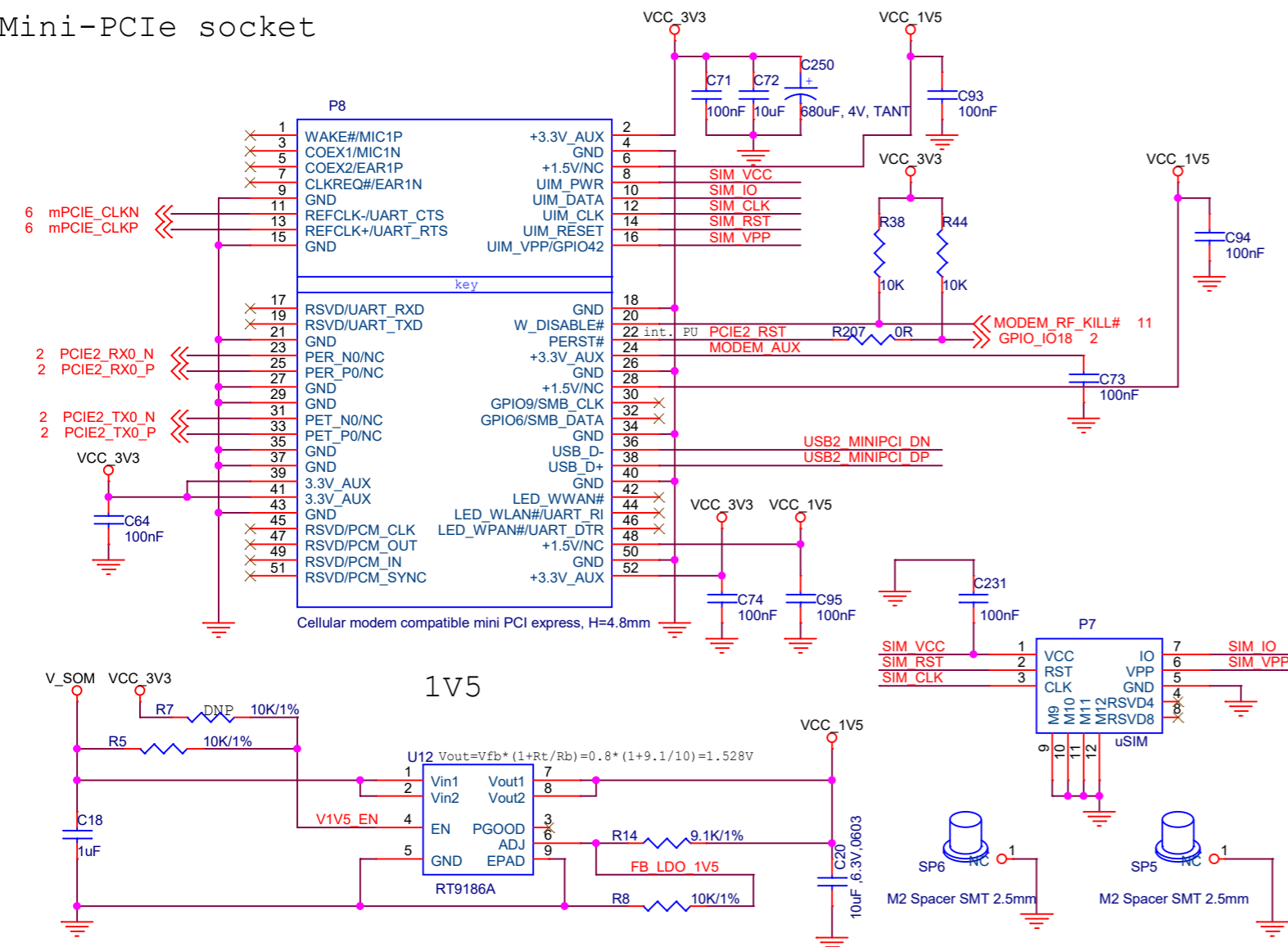
## USB #2 connector



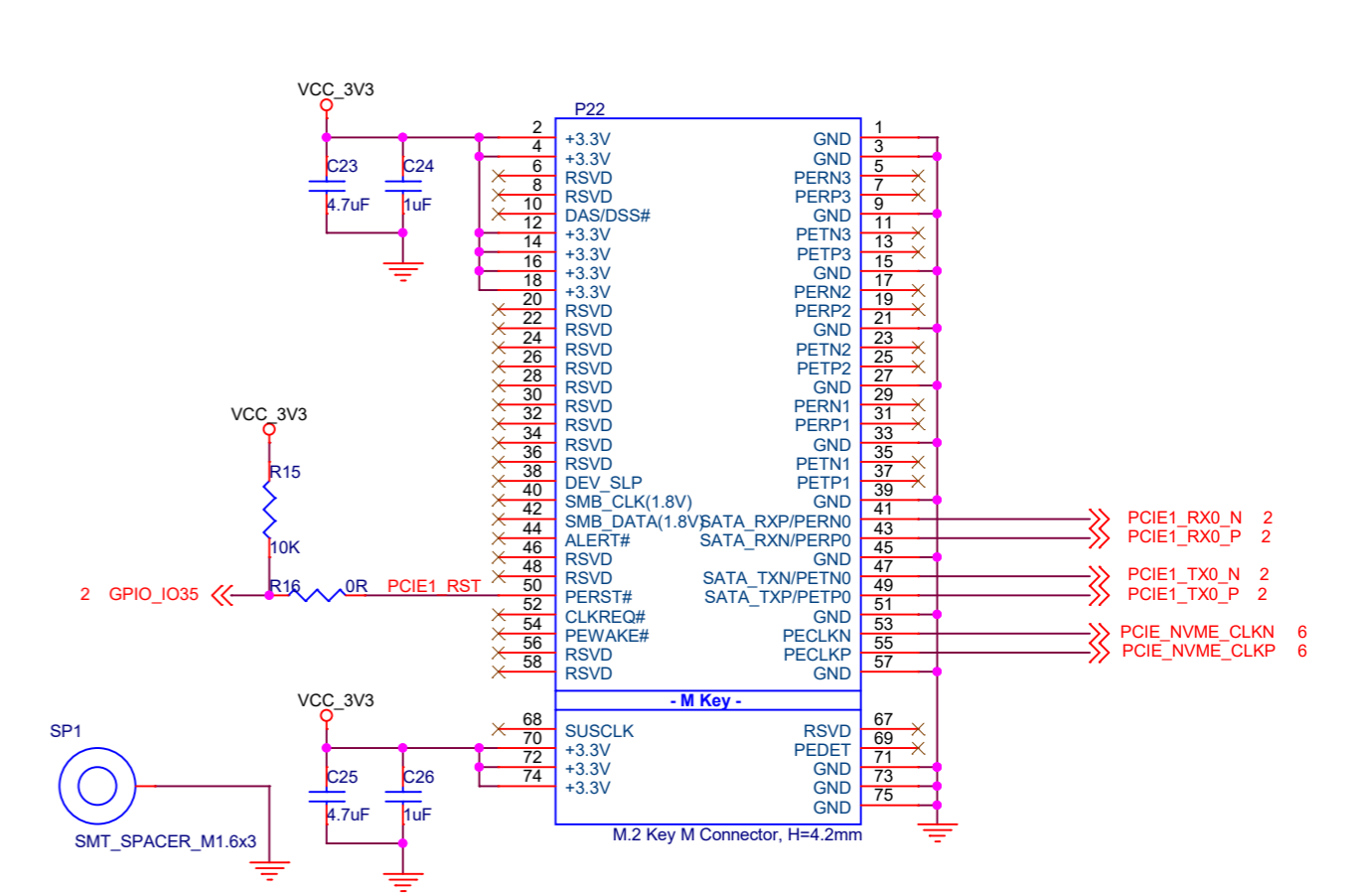
## PCIe clock multiplier



## Mini-PCIe socket

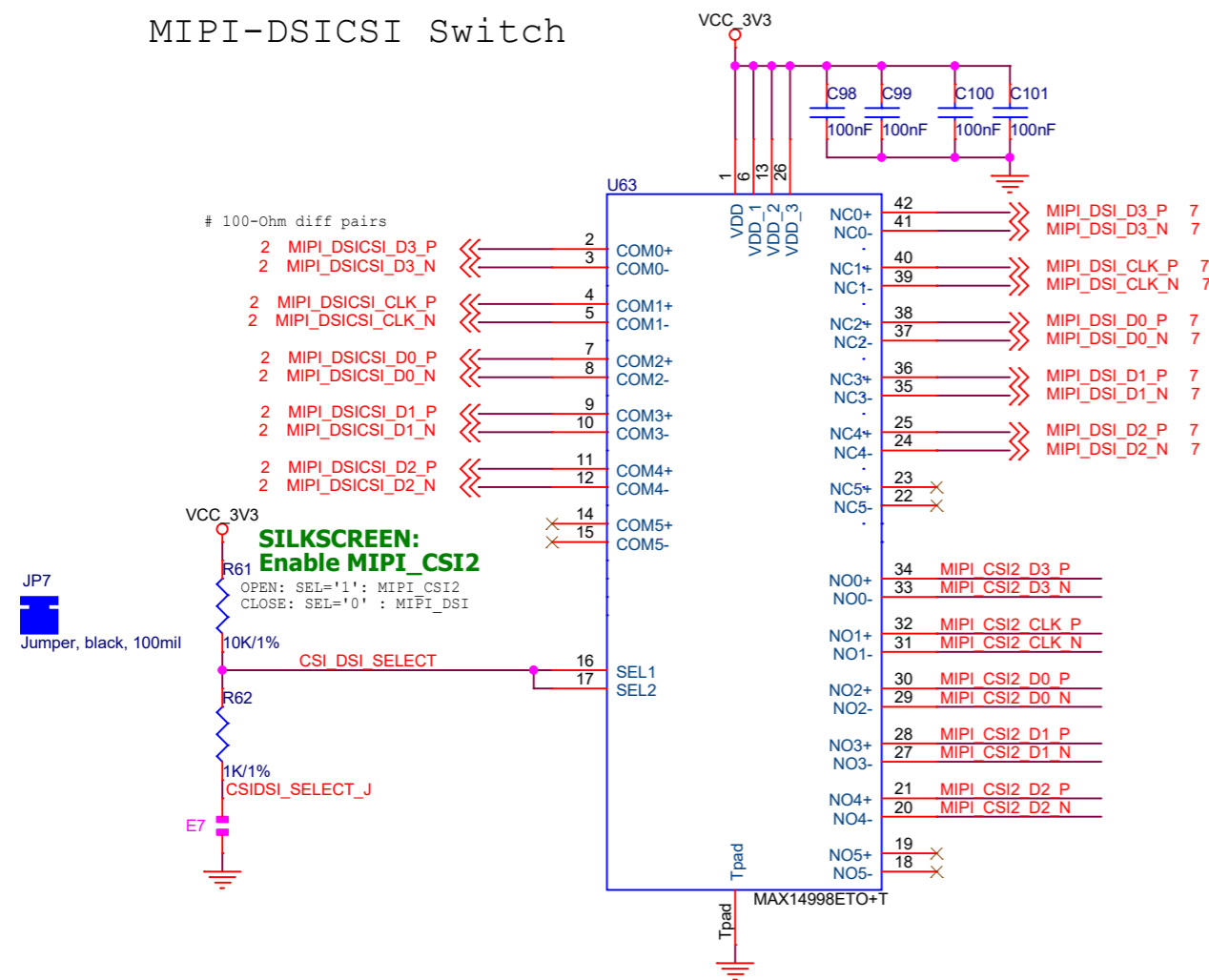


## M.2 for NVME

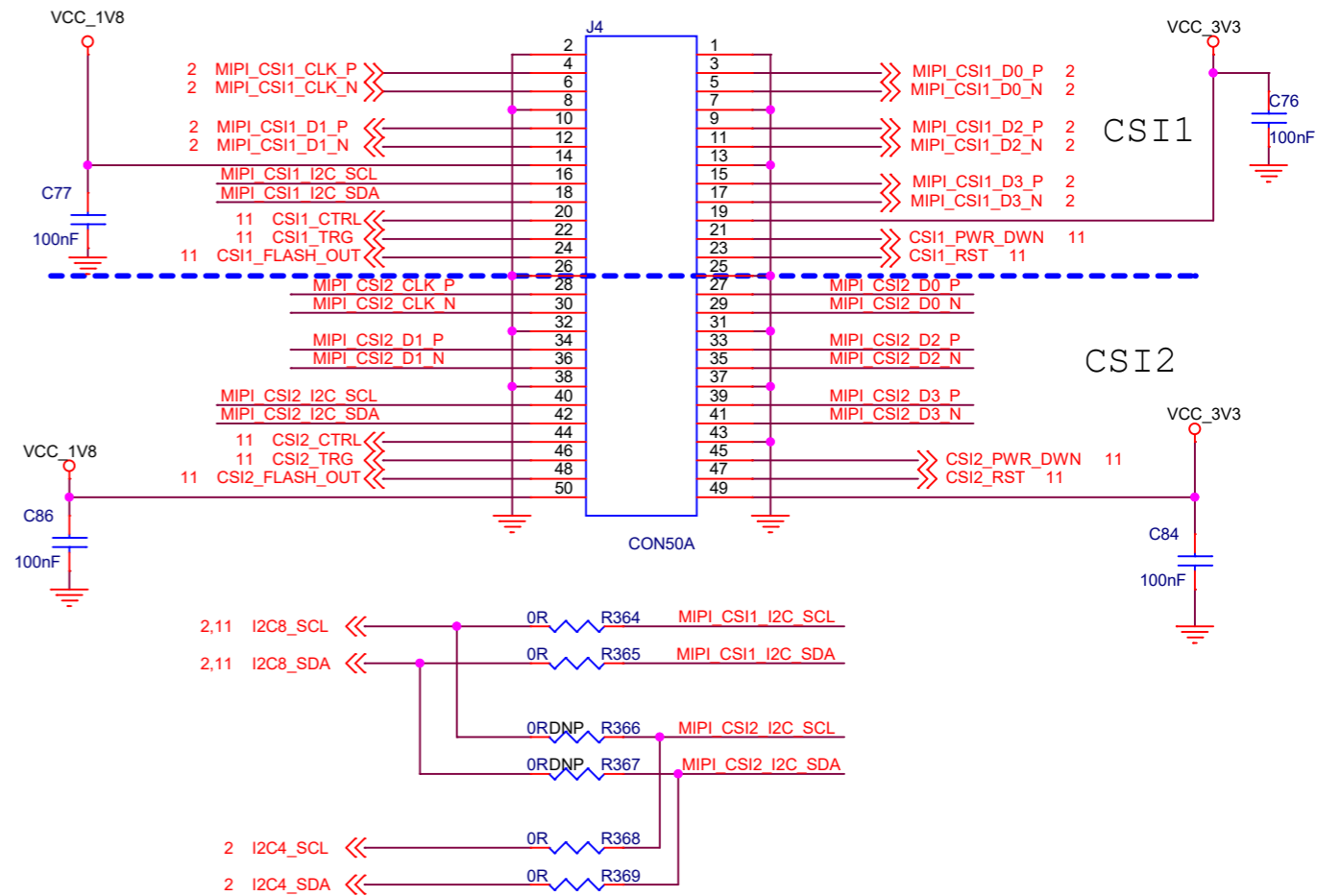




# MIPI-DSICSI Switch

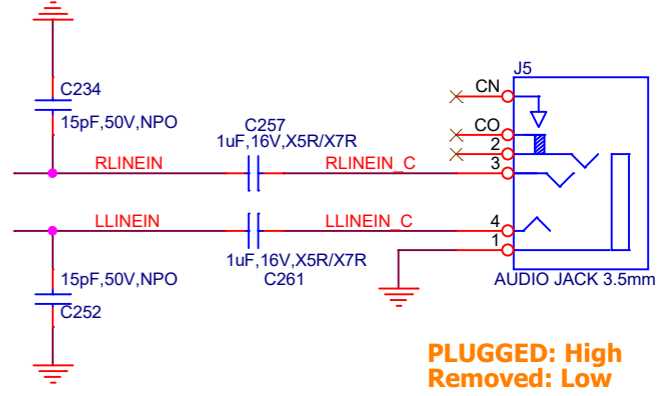
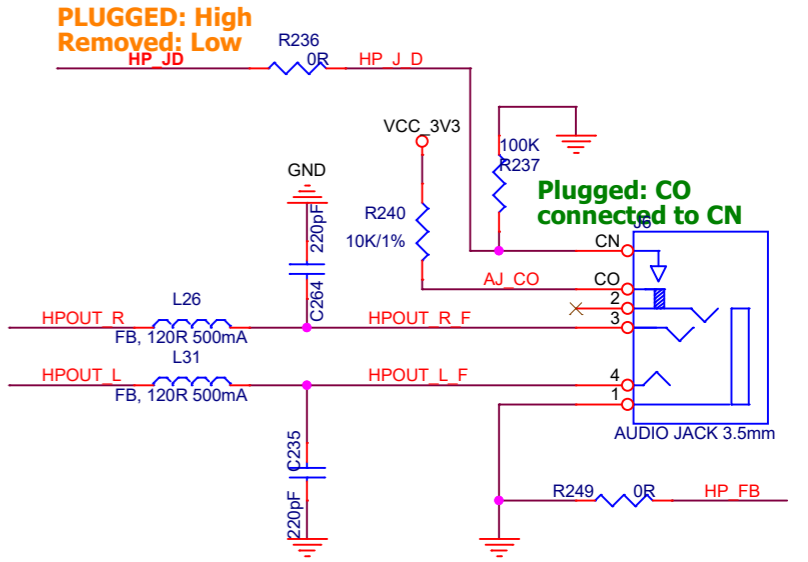
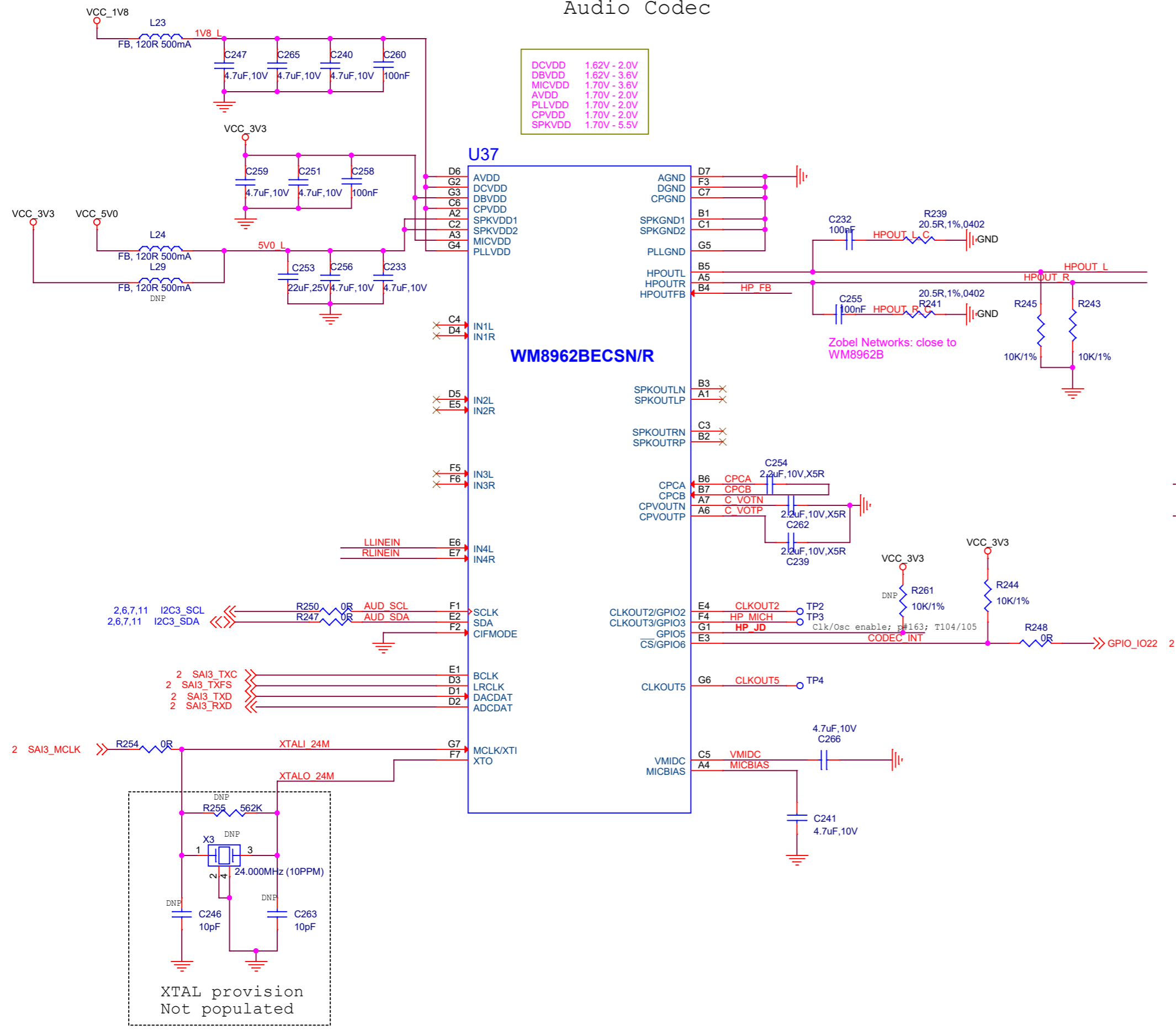


# Camera Edge Connector

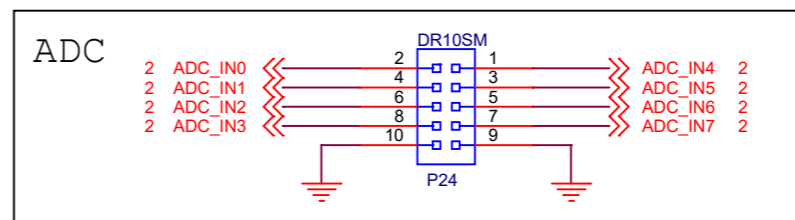
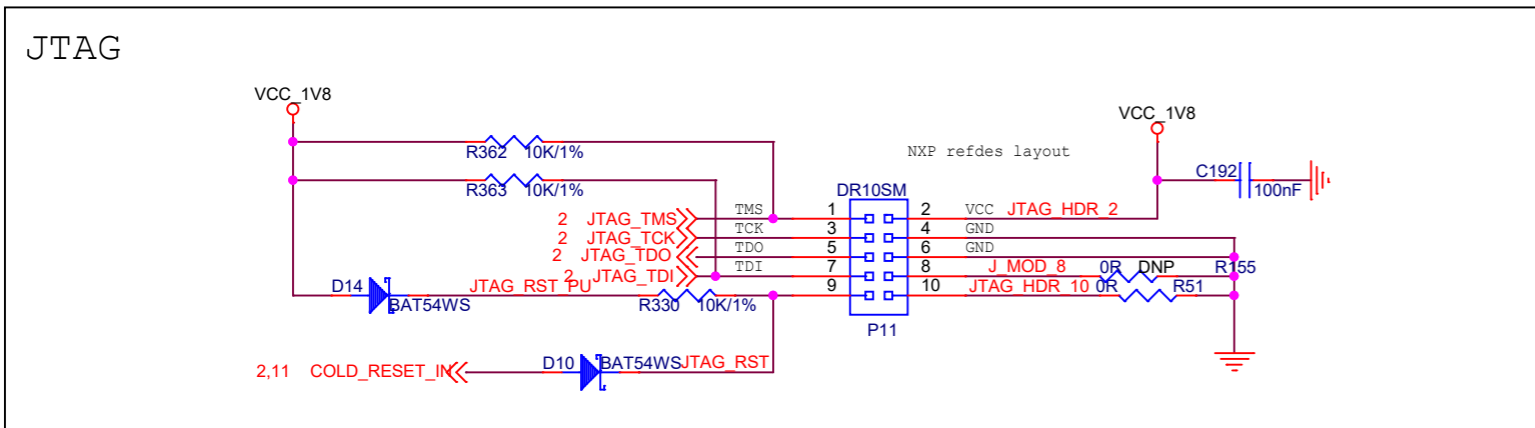
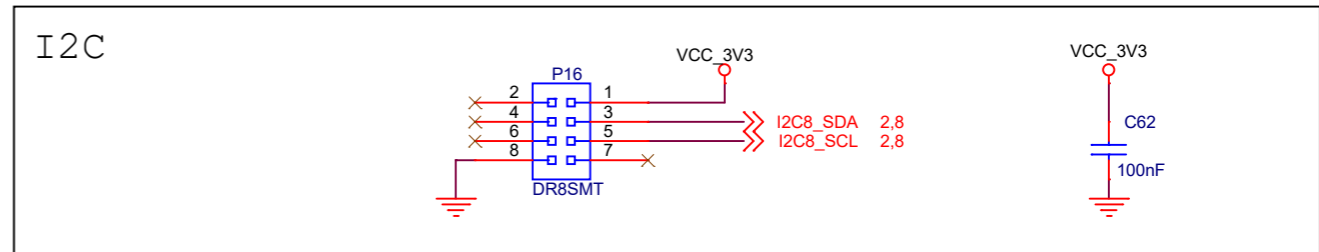
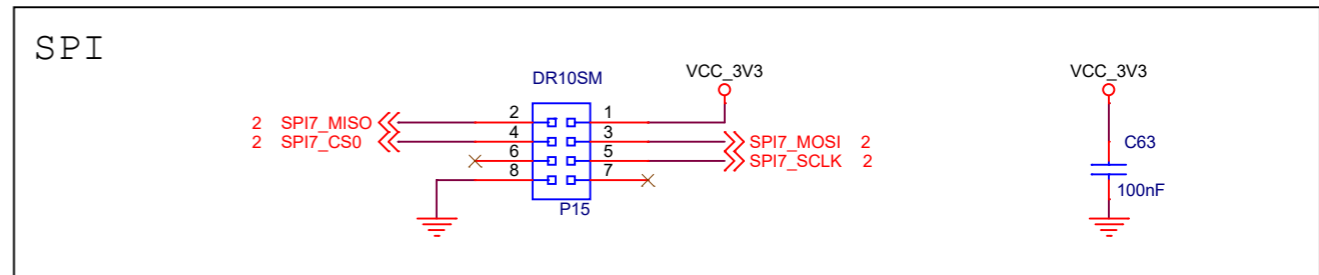
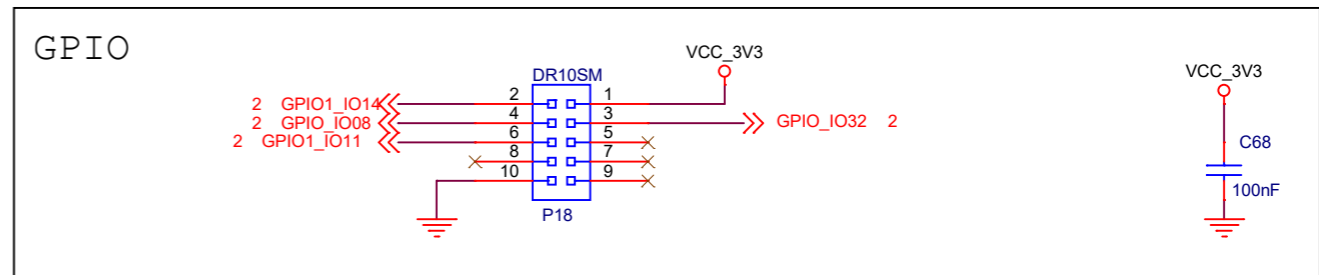
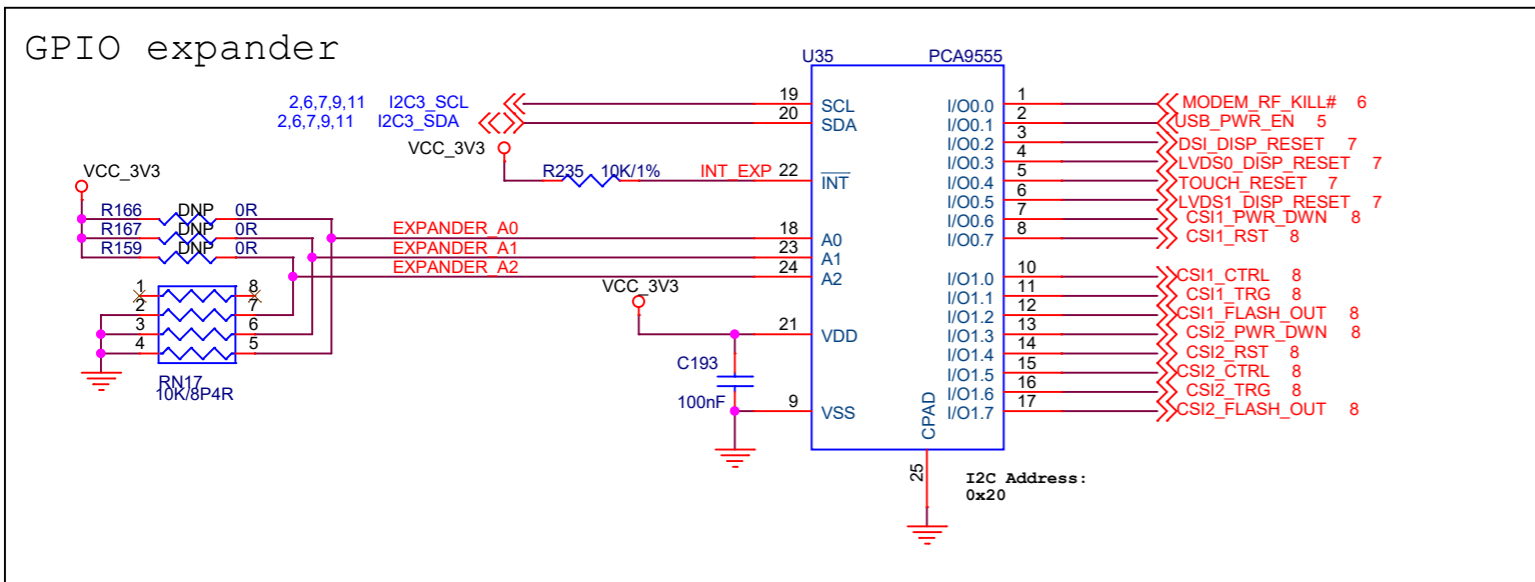
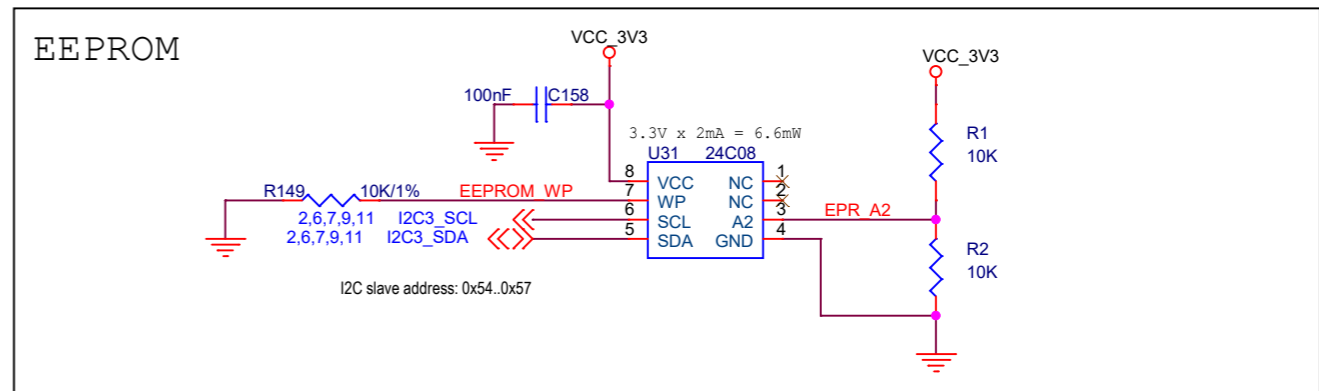
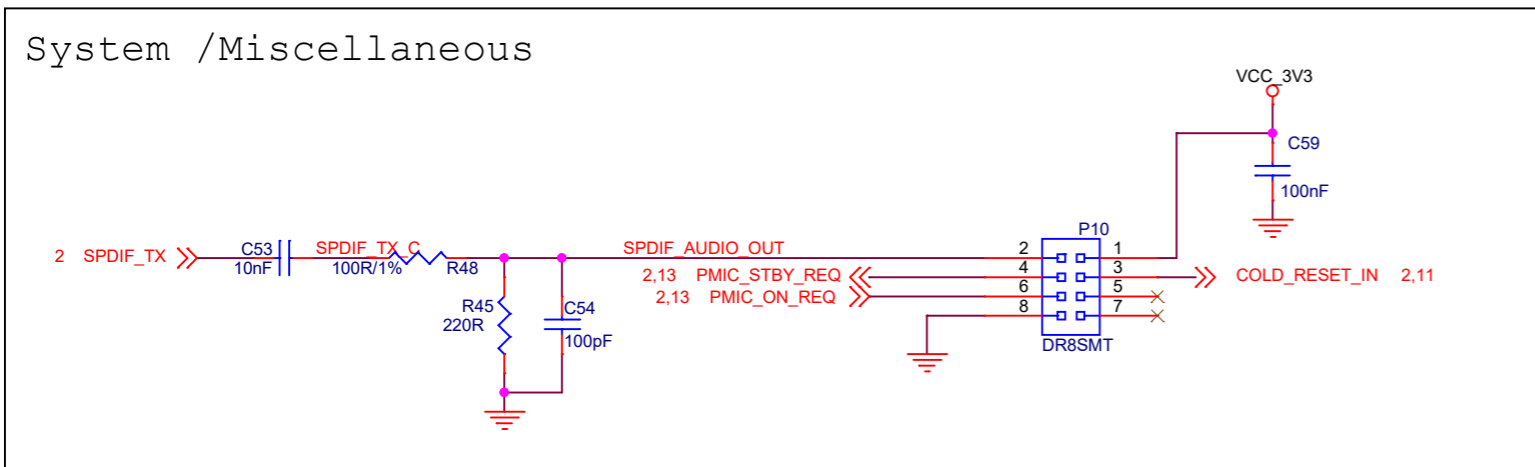
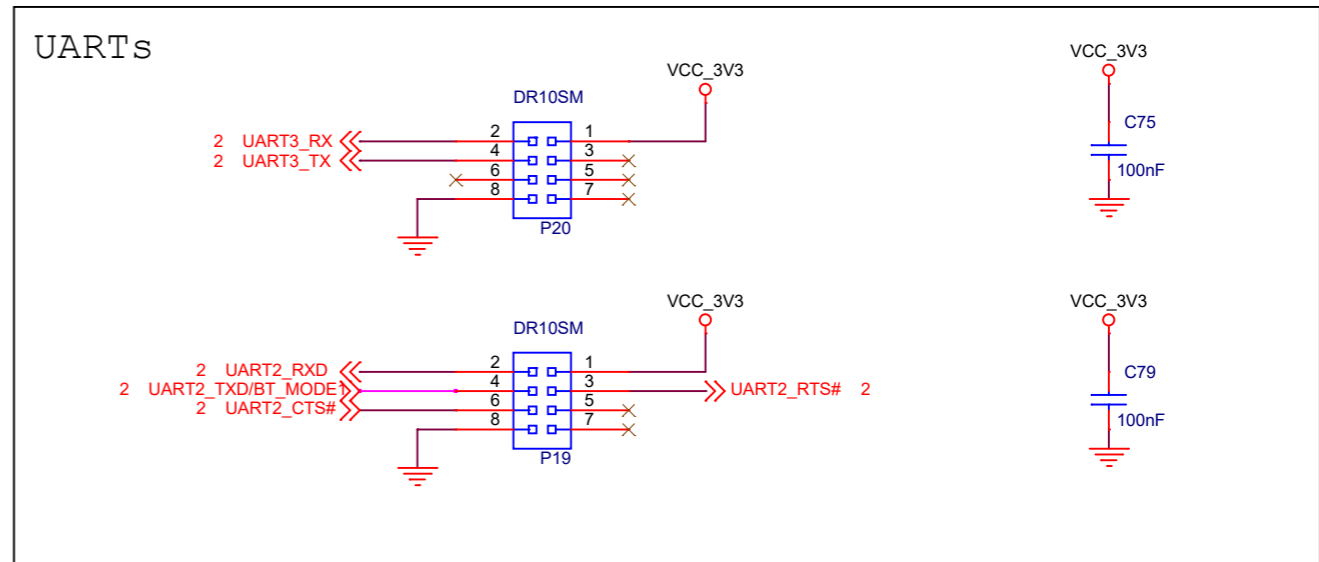
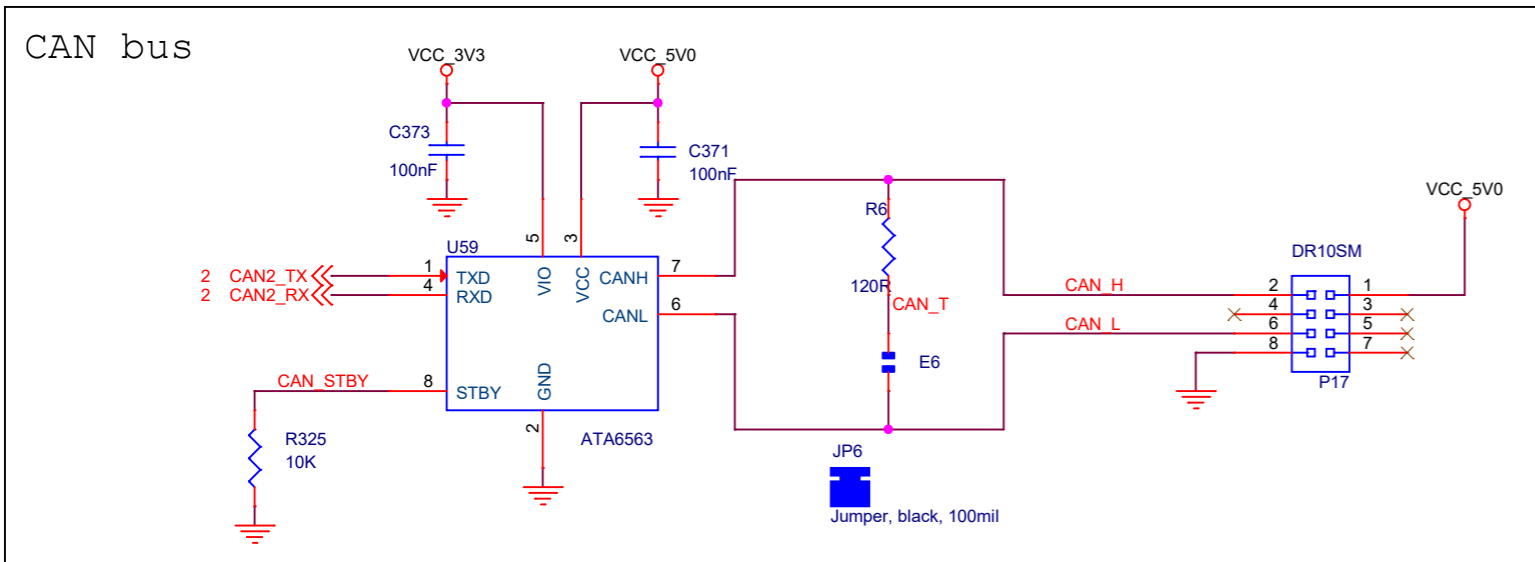


# Audio Codec

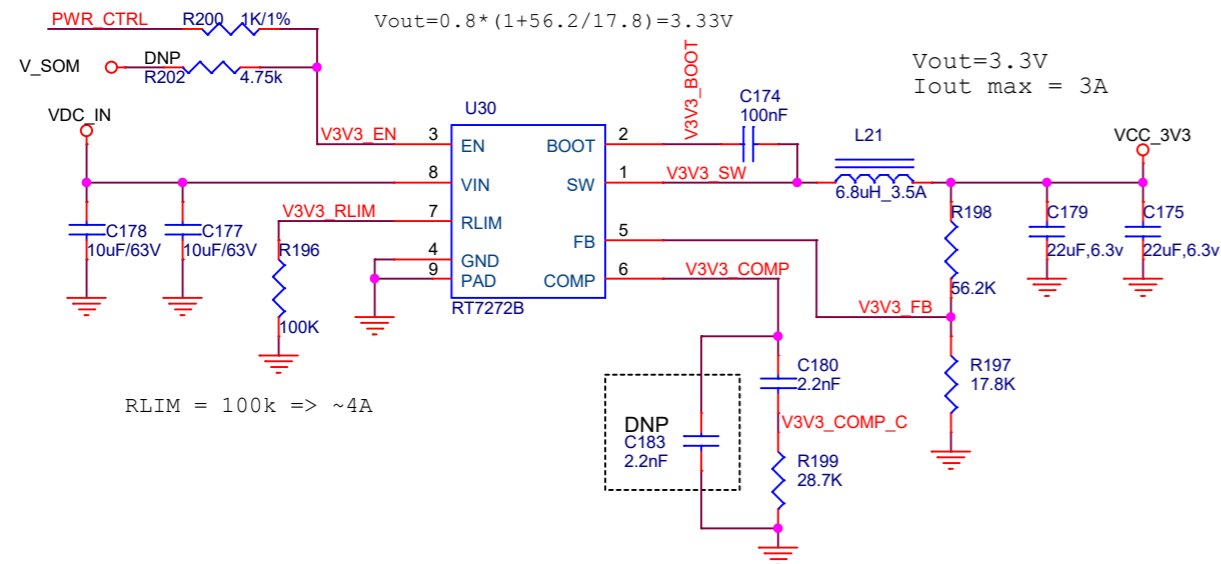
DCVDD	1.62V - 2.0V
DBVDD	1.62V - 3.6V
MICVDD	1.70V - 3.6V
AVDD	1.70V - 2.0V
PLLVD	1.70V - 2.0V
CPVDD	1.70V - 2.0V
SPKVDD	1.70V - 5.5V



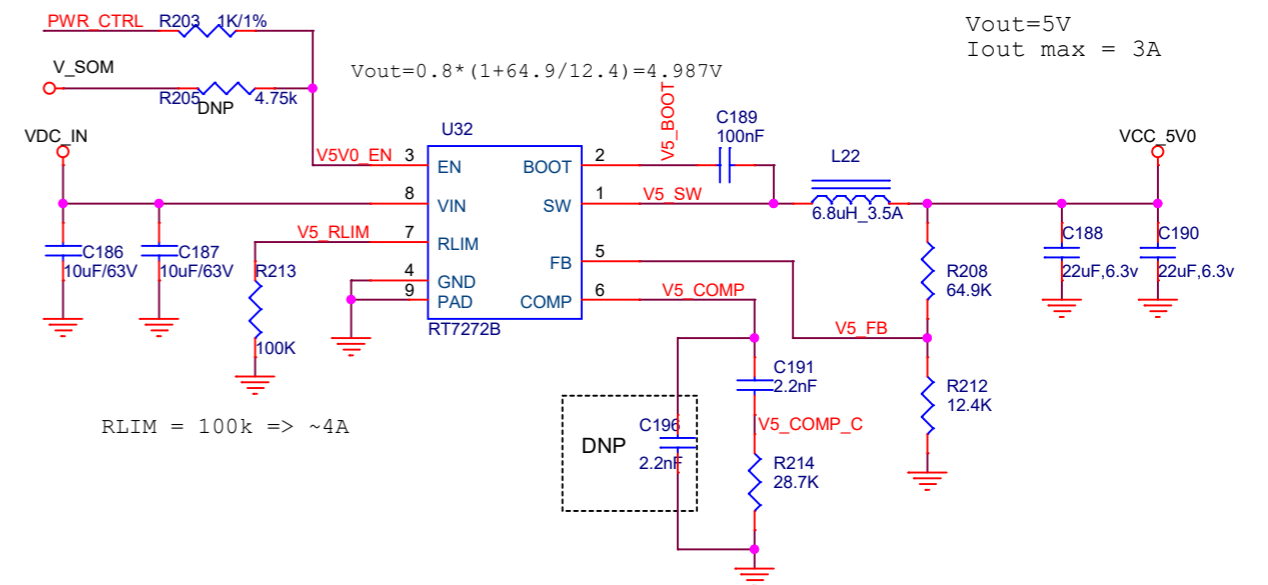




## VCC\_3V3



## VCC\_5V



## V\_SOM (4.5A)

